
EE273 Lecture 5 Noise in Digital Systems

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Today's Assignment

- Problem set 3
 - Dally and Poulton, Exercises 6-3, 6-6, 6-7, 6-13, and 6-16
 - Due at start of class next Wednesday 10/14
- Reading
 - Sections 6.4 through 6.6
 - Complete before class on Monday

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A Quick Overview

- Noise
 - Signals may be corrupted from many sources
 - power supply noise
 - cross talk
 - inter-symbol interference
 - *real* noise (thermal and shot)
 - parameter variation
 - Proportional and independent
- Power Supply Noise
 - Inductance and resistance of supply network cause voltage drops
 - Variation in space on one supply voltage
 - Variation in time on voltage between supplies
- Cross Talk
 - One signal interfering with another signal
 - Capacitive crosstalk between RC lines on a chip
 - floating
 - driven
 - Coupling between LC transmission lines
 - near end
 - far end

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Noise in Digital Systems

Transmitter offsets
Transmitter PS noise

Crosstalk

Inter-Symbol Interference

Signal return noise

Receiver offsets
Receiver PS noise
Reference offset and noise

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Proportional and Independent Noise Sources

- Some noise is *proportional* to signal swing
 - crosstalk
 - inter-symbol interference
 - signal return noise
 - signaling power supply noise
 - if you increase the signal swing, you increase the noise
- Need to cancel this noise, not overpower it!
- Some noise is independent of signal swing
 - receiver sensitivity
 - receiver offset
 - unrelated power supply noise
 - reference offsets

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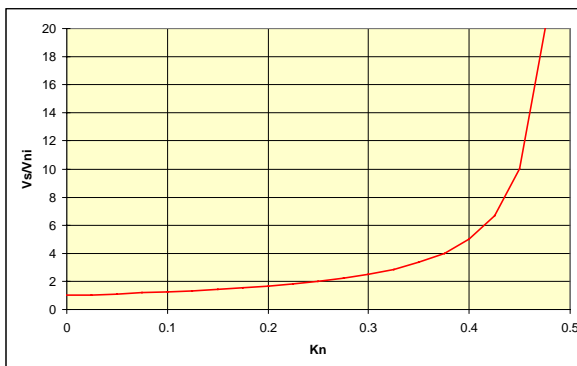
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Proportional and Independent Noise Sources

$$V_N = K_N V_S + V_{NI}$$

$$\frac{V_S}{2} \geq K_N V_S + V_{NI}$$

$$V_S \geq \frac{2V_{NI}}{(1 - 2K_N)}$$



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Power Supply Noise

- The power supply network has parasitic elements
 - on-chip: resistive
 - off-chip: inductive
- Current draw across these elements induces a noise voltage
 - $Ri + Ldi/dt$
- Instantaneous current is what matters
 - may be many times the DC current
 - 10W chip draws 4A at 2.5V
 - peak current may be 10-20A.

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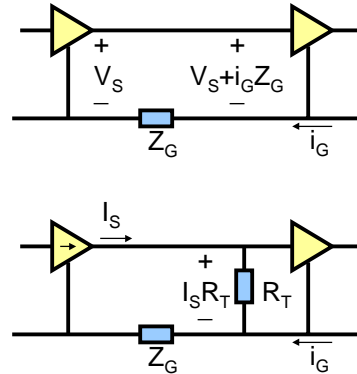
Types of Supply Noise

- Two types of loads
 - logic loads
 - signal loads
- Two types of noise
 - single-supply noise
 - differential-supply noise

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Single Supply Noise

- Voltage drops across parasitics cause variation in the voltage of a single supply (V_{DD} or GND) from one point in the system to another
- If a signal is referenced to the local supply, this variation is additive voltage noise
- The problem can be reduced by using an appropriate reference
 - reference to receiver supply
 - send an explicit reference



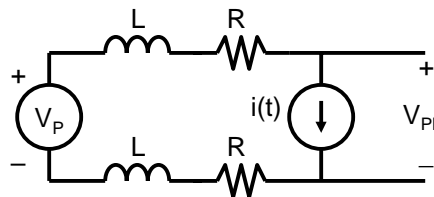
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Differential Supply Noise

- Drops across supply parasitics cause the local supply voltage, V_{PL} , to vary over time
 - affects the delay of many elements
 - systems may not meet timing specifications
 - causes jitter in timing circuits



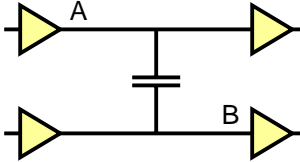
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Cross Talk

- Noise induced by one signal that interferes with another signal
- Capacitive coupling between on-chip lines
- Capacitive *and* Inductive coupling between off-chip lines
- Coupling over shared signal returns








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Cross Talk Between Capacitive Lines

- On-chip wires have significant capacitance to adjacent wires
 - on same layer
 - on adjacent layers
- When adjacent signals change, voltage on a floating line is disturbed
 - capacitive voltage divider
 - signal is not restored

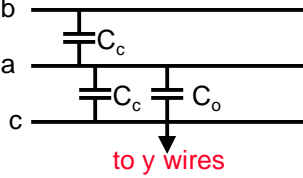
x wires 

y wires 

x wires 

y wires 

a is victim, b and c are aggressors



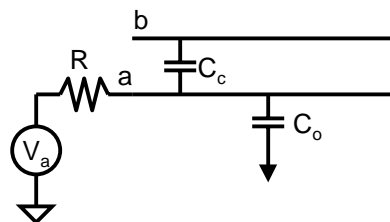
to y wires

$$k_{2c} = \frac{2C_c}{2C_c + C_o}$$

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Capacitive Lines with Drive

- If victim line, a, is driven, a will be disturbed but then restored with time constant $\tau=R(C_c+C_o)$
- If rise on aggressor, b, is slow compared to τ , magnitude of disturbance is reduced



$$\left(\frac{\tau}{t_r}\right)\left(1-\exp\left(-\frac{t_r}{\tau}\right)\right)$$



$$\tau=R(C_c+C_o)$$

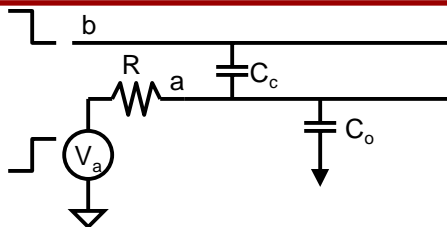
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Cross Talk and Delay

- Capacitive cross talk can affect delay of RC signals
- If aggressor(s) switch in opposite direction, effective capacitance of C_c is doubled
- If aggressor(s) switch in the same direction, C_c is effectively eliminated
- Can cause 2:1 variation in delay in some cases
- Significant cause of jitter if timing is critical



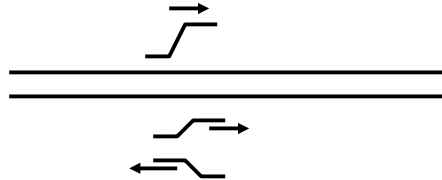
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Transmission Line Cross Talk

- A signal transition on one transmission line induces forward and reverse traveling waves on adjacent transmission lines

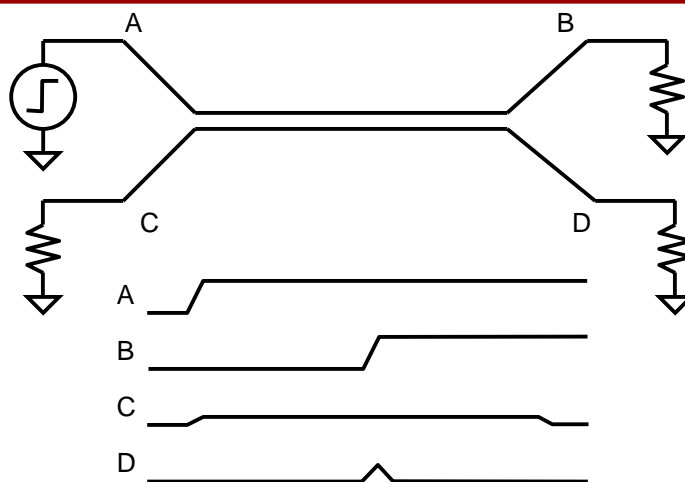


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Transmission Line Cross Talk Experiment

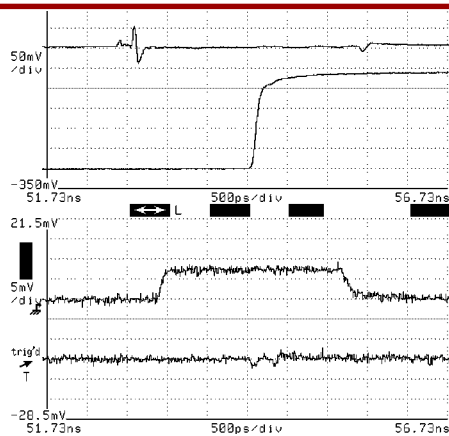


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Experimental Cross Talk Measurements



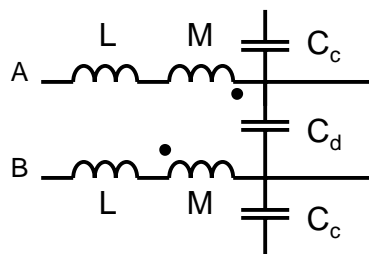
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Inductive and Capacitive Coupling

- Consider a positive transient on aggressor line, A
- Capacitive coupling induces a *voltage* on victim line B
 - positive waves in both forward and reverse directions
- Inductive coupling induces a *current* in line B
 - positive wave in the reverse direction
 - negative wave in the forward direction



$$k_{lx} = \frac{M}{L}$$

$$k_{cx} = \frac{C_d}{C}$$

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Near-end and Far-end Crosstalk

- Inductive and capacitive coupling *add* at the **near** end of the line
 - both waves are positive
 - pulse begins at beginning of coupled section
 - pulse width equals length of coupled section
- Inductive and capacitive coupling *subtract* at the **far** end of the line
 - in a homogeneous medium cancellation is exact
 - narrow pulse coincident with wave on agressor

inductive coupling

capacitive coupling

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Near End and Far End Crosstalk The Equations

$$k_{rx} = \frac{k_{cx} + k_{lx}}{4}$$

$$k_{fx} = \frac{k_{cx} - k_{lx}}{2}$$

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Next Time

- Signal return crosstalk
- Intersymbol interference
- Managing noise