EE382C Advanced Computer Organization: Interconnection Networks
Course Policy and Information

Room: Room 128 in the school of Education building
Tuesday/Thursday 3:15-4:30

Instructor: William J. Dally
dally@stanford.edu
Gates 301
(650) 725-8945
Hours: Tuesday 1:30-2:30 or by appointment

TAs: George Michelogiannakis
mihelog@stanford.edu
Gates 218
(650) 725-3646
Monday 2:30 – 3:30 or by appointment

Daniel Becker
dub@stanford.edu
Gates 218
(650) 725-3646
Wednesday 2:30 – 3:30 or by appointment

Support: TBD

Web: http://cva.stanford.edu/ee382c

Goal
EE382C investigates topics in interconnection network architecture and design including network topology, routing strategies, flow control methods, deadlock and deadlock avoidance, congestion control, quality of service, and router architecture. We will examine applications of networks to parallel computer interconnect, main-memory interconnect in multiprocessors, data-center networking, and switching fabric in Internet routers. EE382C is a completely different course than EE382A, which is offered in alternate years and deals with the architecture of high-performance processors.

Assignments
There will be three homework assignments, a project, and a research paper. The homework assignments will cover the basics of interconnection networks. The project will involve designing some aspect of an interconnection network. For the research paper you will be asked to investigate a current topic in interconnection networks, write a short paper on the topic, and present your results to the class.

Late Assignments
Homework is due at the beginning of class on the due date. There will be no credit given for late homework assignments.
**Collaboration**

Collaboration on homework assignments, projects and research papers is encouraged subject to the following guidelines:

1. No more than four people can collaborate on a homework solution.
2. Groups of people working together should submit a single homework solution for the group.
3. Any assistance received in the solution of a homework assignment should be acknowledged in writing on the homework assignment.

**Exams**

There will be a Midterm exam held during the class period on **May 3**. The midterm will cover material up through that presented on April 28. Alternative exam times will be made available only under extreme circumstances. Requests to re-grade exams or homework must be submitted in writing within one week of the exam date. An exam submitted for re-grading might have all questions re-graded, not just the one(s) requested.

There will be NO final exam.

**Grading**

- Homework Assignments 20%
- Midterm 20%
- Project 30%
- Research Paper 20%
- Class Participation 10%

**Honor Code**

We expect all students to act in the spirit of the Stanford Honor Code. Please see http://www.stanford.edu/dept/vpsa/judicialaffairs/guiding/honorcode.htm if you are not already familiar with the honor code.

**Text**


**Prerequisites**

EE282 or permission of the instructor.